

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE PATENT APPLICATION OF : Karl FECTEAU *et al.*
SERIAL No. : 10/022,856
FILING DATE : December 20, 2001
ART UNIT : 2637
EXAMINER : QUTBUDDIN GHULAMALI
FOR : METHODS, APPARATUS, AND SYSTEMS FOR REDUCING
INTERFERENCE ON NEARBY CONDUCTORS

STATEMENT OF FACTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby declare as follows:

1. I have reviewed our firm's records pertaining to the above-identified application. Relevant copies of these records are attached. Our records show the following:
 - a. We filed the above-captioned application on December 20, 2001.
 - b. The above-captioned patent application was assigned to Hyperchip, Inc. by means of an assignment document recorded with the U.S. Patent and Trademark Office on March 18, 2002, at Reel/Frame 012697/0374 (See Exhibit A).
 - c. We were instructed by Mr. C. Marc Benoit, IP Manager, of Hyperchip, Inc. that Hyperchip would be handling future prosecution of the above-captioned application and were asked to return the files to Hyperchip, Inc. (See Exhibit B).
 - d. On or about July 25, 2002, we forwarded the file for the above-captioned patent application to Hyperchip, Inc. (See Exhibit C).
 - e. A Notice of Allowance dated August 25, 2005, was received in our office on August 26, 2005 (See Exhibit D).
 - f. On August 29, 2005, we forwarded the Notice of Allowance to

Hyperchip, Inc. by First-Class Mail to our last known address of record (See Exhibit E).

g. Our letter of August 29, 2005 to Hyperchip, Inc., was subsequently returned to us.

h. We subsequently received a Notice of Abandonment, dated January 18, 2006, for the above-captioned application (See Exhibit F).

i. On January 26, 2006, we forwarded the Notice of Abandonment to Hyperchip, Inc. by First-Class Mail (See Exhibit G).

j. Our letter of January 26, 2006 was returned to us (See Exhibit H).

2. I hereby acknowledge that willful false statements and the like are punishable by fine or imprisonment, or both (18 U.S.C. § 1001) and may jeopardize the validity of the present application or any patent issuing thereon. All statements made of my knowledge are true and all statements made on information and belief are believed to be true.

Respectfully submitted,
PILLSBURY WINTHROP SHAW PITTMAN LLP

By: 

Bryan P. Collins
Registration No. 43,560
Tel. No. 703.770.7538
Fax No. 703.770.7901

Date: 8/31/09
P.O. Box 10500
McLean, VA 22102
703.770.7900

EXHIBIT A



United States Patent and Trademark Office

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Assignments on the Web > Patent Query

Patent Assignment Abstract of Title

NOTE: Results display only for issued patents and published applications. For pending or abandoned applications please consult USPTO staff.

Total Assignments: 3

Patent #: NONE Issue Dt: Application #: 10022856 Filing Dt: 12/20/2001
Publication #: 20030117184 Pub Dt: 06/26/2003
Inventors: Karl Fecteau, Claude Thibeault, Yvon Savaria, Yves Blaquiere, Jean-Jacques Laurin et al
Title: METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS

Assignment: 1

Reel/Frame: 012697/0374 Recorded: 03/18/2002 Pages: 7

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: FECTEAU, KARL

Exec Dt: 03/14/2002

THIBEAULT, CLAUDE

Exec Dt: 03/15/2002

SAVARIA, YVON

Exec Dt: 03/14/2002

BLAQUIERE, YVES

Exec Dt: 03/14/2002

LAURIN, JEAN-JAQUES

Exec Dt: 03/14/2002

JIN, ZHONG-FANG

Exec Dt: 03/14/2002

Assignee: HYPERCHIP INC.

1800 BOUL, RENE-LEVESQUE OUEST
MONTREAL, QUEBEC, CANADA H3H 2

Correspondent: PILLSBURY WINTROP LLP
KERRY HARTMAN
INTELLECTUAL PROPERTY GROUP
1600 TYSONS BOULEVARD
MCLEAN, VA 22102

Assignment: 2

Reel/Frame: 015408/0178 Recorded: 11/24/2004 Pages: 5

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: 4198638 CANADA INC

Exec Dt: 10/14/2004

Assignee: NORMAN, RICHARD S.

1877 CHEMIN POISSANT SUTTON
QUEBEC, CANADA JOE 2KO

Correspondent: OGILVY RENAULT
C. MARC BENOIT
1981 MCGILL COLLEGE AVENUE, SUITE 1600
MONTREAL, CANADA H3A2Y-3

Assignment: 3

Reel/Frame: 015408/0203 Recorded: 11/24/2004 Pages: 8

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HYPERCHIP INC.

Exec Dt: 10/14/2004

Assignee: 4198638 CANADA INC.

4028 MARLOWE
MONTREAL, QUEBEC, CANADA H4A 3M2

Correspondent: 4198638 CANADA INC.

C. MARC BENOIT
4028 MARLOWE, MONTREAL, QUEBEC
CANADA, H4A3M2

Search Results as of: 08/26/2009 05:33 PM

If you have any comments or questions concerning the data displayed, contact PRD / Assignments at 571-272-3350.
Web interface last modified: October 18, 2008 v.2.0.2

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EXHIBIT B



HYPERCHIP
THE PETABIT ROUTING COMPANY

Montreal, May 31, 2001

Via Courier

Mr. Jeffrey Karceski
PILLSBURY WINTHROP
1600 Tysons Blvd.,
McLean, VA 22102

Re: Transfer of Hyperchip files
Your/our refs: see attached list

Dear Jeff,

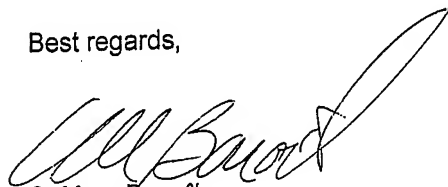
We regret to inform you that, for cost and efficiency reasons, Hyperchip needs to bring as much intellectual property work in-house as possible.

Accordingly, effective immediately, Hyperchip will be handling drafting, prosecution and maintenance of the patent files on the attached list.

Please coordinate the return of all material and work in progress associated with these files with the undersigned. Also, kindly advise us before incurring any further costs.

We thank you for your professional services and hope to have further dealings with you in the future.

Best regards,



C. Marc Benoit
IP Manager

CMB/al

Enclosure: List of references

Cc: Kerry Hartman

List of Hyperchip's Files

Pillsbury File #	HC File #	Application No.	Title	Status
276792	P(US)2001-019	US 10/022,851	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
283335	P(US)2001-086		Synchronization of Data Transfers	In Drafting
290547	P(US)2001-199	US 10/022,856	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
290548	P(US)2001-200	US 10/022,852	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
290549	P(US)2001-201	US 10/023,478	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam

EXHIBIT C

38700
Hyperchip



PILLSBURY WINTHROP LLP

1600 TYSONS BOULEVARD MCLEAN, VA 22102 703.905.2000 F: 703.905.2500

Jeffrey A. Cox
703.905.2048
jcox@pillsburywinthrop.com

July 25, 2002

Mr. Marc Benoit
Hyperchip Inc.
1800 boul. Rene'-Levesque, ouest
Montreal, Quebec H3H 2H2
Canada

~~LegalKey Processed~~

LegalKey Processed
Re-updated 07-10-03
gina Berrios
processed

Re: Hyperchip - 037800
38700 ²¹

Dear Mr. Benoit:

Pursuant to instructions from the client, we are transferring the following trademark and/or patent file(s) to you herewith:

Matter No	Appln/Serial No.	Country
✓ 276792 ✓	10/022,851	US
✓ 290549 ✓	10/023,478	US
✓ 290548 ✓	10/022,852	US
✓ 290547 ✓	10/022,856	US
283335 ✓		US

You are advised that Pillsbury Winthrop LLP assumes no further responsibility for these files including, but not limited to, future prosecution and/or annuities. Please file the appropriate documents to ensure that future correspondence is sent directly to you. A docket sheet of all relevant due dates is also attached (if applicable).



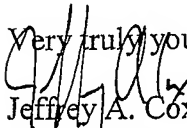
PILLSBURY WINTHROP LLP

July 25, 2002

Page 2

These files are being transferred with the understanding that access will be provided to Pillsbury Winthrop LLP should the need arise at a future date. Please sign and return a copy of this letter to indicate your agreement and receipt of the above files.

Very truly yours,


Jeffrey A. Cox
Records Manager

znm

Enclosures

cc: Hyperchip

EXHIBIT D



UNITED STATES PATENT & TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

909 7590 08/25/2005

PILLSBURY WINTHROP SHAW PITTMAN, LLP
P.O. BOX 10500
MCLEAN, VA 22102

EXAMINER

GHULAMALI, QUTBUDDIN

ART UNIT

PAPER NUMBER

2637

DATE MAILED: 08/25/2005

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,856	12/20/2001	Karl Fecteau	P 290547	1436

TITLE OF INVENTION: METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	11/25/2005

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. **PROSECUTION ON THE MERITS IS CLOSED.** THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. **THIS STATUTORY PERIOD CANNOT BE EXTENDED.** SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B - Fee(s) Transmittal should be completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

4

RECEIVED
AUG 26 2005
PILLSBURY WINTHROP SHAW PITTMAN
MCLEAN, VA

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571) 273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

909 7590 08/25/2005

PILLSBURY WINTHROP SHAW PITTMAN, LLP
P.O. BOX 10500
MCLEAN, VA 22102

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,856	12/20/2001	Karl Fecteau	P 290547	1436

TITLE OF INVENTION: METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	11/25/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
GHULAMALI, QUTBUDDIN	2637	375-288000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
- (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1	_____
2	_____
3	_____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☐ Issue Fee
- ☐ Publication Fee (No small entity discount permitted)
- ☐ Advance Order - # of Copies _____

4b. Payment of Fee(s):

- ☐ A check in the amount of the fee(s) is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☐ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FBES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,856	12/20/2001	Karl Fecteau	P 290547	1436
909	7590	08/25/2005	EXAMINER	
PILLSBURY WINTHROP SHAW PITTMAN, LLP			GHULAMALI, QUTBUDDIN	
P.O. BOX 10500			ART UNIT	
MCLEAN, VA 22102			PAPER NUMBER	

2637

DATE MAILED: 08/25/2005

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 917 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 917 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571) 272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.

Notice of Allowability

Application No.

10/022,856

Examiner

Qutub Ghulamali

Applicant(s)

FECTEAU ET AL.

Art. Unit

2637

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 12/20/2001.
2. ☒ The allowed claim(s) is/are 1-44.
3. ☒ The drawings filed on 20 December 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 5/30/02
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

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DETAILED ACTION

1. This Office Action is in response to application filed 12/20/2001

Allowable Subject Matter

2. Claims 1-44 allowed.

3. The following is an examiner's statement of reasons for allowance:

Regarding claims 1, 8, 14, 19, 25, 32 and 38, the prior art (Nomura, USP 6,128,347), discloses a signal transmission circuit with protection line driven signal to mitigate crosstalk among signals transmitting corresponding first and second sets of signals over in transmission lines. Nomura, however, in combination with other claimed limitations fail to render obvious the data transmission method comprising:

receiving a first set of input signals and a second set of input signals, each input signal having a series of state transitions synchronized to a data clock signal having a period T_{CLK} ;

transmitting corresponding first and second sets of output signals such that each output signal (1) corresponds to an input signal of the corresponding set, (2) passes along a corresponding one of a corresponding set of a plurality of conductive paths, and (3) has a series of state transitions corresponding to the series of state transitions of the corresponding input signal;

transmitting each among a third set of output signals on a corresponding one of a third set of the plurality of conductive paths; and

transmitting each among a fourth set of output signals on a corresponding one of a fourth set of the plurality of conductive paths, wherein adjacent conductive paths of the first set are separated

Art Unit: 2637

by at least one conductive path of the third set, and wherein adjacent conductive paths of the second set are separated by at least one conductive path of the fourth set, and wherein a time between a state transition on an input signal of the first set and the corresponding state transition on the corresponding output signal exceeds a time between a state transition on an input signal of the second set and the corresponding state transition on the corresponding output signal by a delay period T_{DLY} , and wherein the period T_{CLK} is greater than the delay period T_{DLY} , and wherein a first of two ends of each one among the plurality of conductive paths is closer to the first end of an adjacent conductive path than to the second end of the adjacent conductive path, and wherein said transmitting each among the first and second sets of signals includes applying the signal to the first end of the corresponding conductive path, and wherein said transmitting each among the third and fourth sets of signals includes applying the signal to the second end of the corresponding conductive path.

Such limitations, as recited with variations, to claims 1, 8, 14, 19, 25, 32 and 38, is neither anticipated nor rendered obvious by the prior art.

4. Claims 2-7, 9-13, 15-18, 20-24, 26-31, 33-37 and 39-44 are allowed by virtue of their dependency to claims highlighted above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US Patents:

Malerevich et al (USP 6611538) discloses a data transmission synchronization system with feedback.

Schipper et al (USP 6128337) shows multipath signal discrimination by use of a bit pattern that cannot be included in a direct signal pattern.

Lincoln (US Pub. 2005/0069041) discloses a coherent expandable high speed interface for transmitting continuous parallel data streams.

Publications:

Masayuki Mizuno et al, "Clock Distribution Network with On-Chip Transmission Lines",

Proceedings of the IEEE 2000 International conference, 5-7 June 2000, Page(s) 3 - 5.

Mikazuki, T.; Matsui, N.; "Statistical design techniques for high-speed circuit boards with correlated structure distributions", Components, Hybrids, and Manufacturing Technology, IEEE Transactions on Components, Packaging, and Manufacturing Technology, Part A, B, C, Volume 14, Issue 3, Sept. 1991 Page(s) 512 - 517.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Qutub Ghulamali whose telephone number is (571) 272-3014.

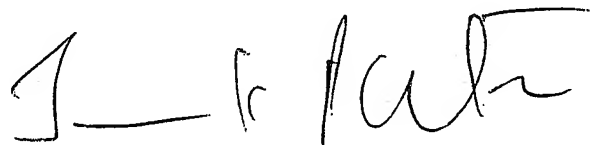
Art Unit: 2637

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571) 272-2988. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


QG.

August 18, 2005.



JAY K. PATEL
SUPERVISORY PATENT EXAMINER

Atty.
Dkt. No.

M#

Client Ref.

0290547

P(US)2001-199

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Applicant: FECTEAU et al.

Appln. No.: 10/022,856

Filing Date: December 20, 2001

Date: May 30, 2002

Page 1 of

Examiner:

Group Art Unit: 2631

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
QG	AR 5,101,347	03/1992	BALAKRISHNAN et al.			
QG	BR 5,815,031	09/1998	TAN et al.			
QG	CR 5,886,943	03/1999	SEKIGUCHI et al.			
QG	DR 5,892,981	04/1999	WIGGERS			
QG	ER 5,994,766	11/1999	SHENOY et al.			
QG	FR 5,994,946	11/1999	ZHANG			
QG	GR 6,008,705	12/1999	GHOSHAL			
QG	HR 6,015,300	01/2000	SCHMIDT, JR. et al.			
QG	IR 6,081,146	06/2000	SHIOCHI et al.			
QG	JR 6,110,221	08/2000	PAI et al.			
QG	KR 6,114,890	09/2000	OKAJIMA et al.			
QG	LR 6,184,702 B1	02/2001	TAKAHASHI et al.			

RECEIVED

JUN 03 2002

Technology Center 2600

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						English Abstract		Translation Readily Available		
		Document Number	Date MM/YYYY	Country	Inventor Name		Enc	No	Enc	No
OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)										
QG	MR	Ismail et al., Repeater Insertion in Tree Structured Inductive Interconnect, Proceedings of the 1999 International Conference on Computer-aided Design, November 1999, pp. 420-424.								
QG	NR	Ismail et al., Effects of Inductance on the Propagation Delay and Repeater Insertion in VLSI Circuits, Proceedings of the 36th ACM/IEEE Conference on Design Automation Conference, June 1999, 4 pages.								
QG	OR	Alpert et al., Buffer Insertion With Accurate Gate and Interconnect Delay Computation, Proceedings of the 36th ACM/IEEE Conference on Design Automation Conference, June 1999, 6 pages.								
QG	PR	Alpert et al., Buffer Insertion for Noise and Delay Optimization, Proceedings of the 35th annual conference on Design Automation Conference, May 1998, pp. 362-367								
QG	QR	Davari et al., CMOS Scaling for High Performance and Low Power - The Next Ten Years, Proceedings of the IEEE, vol. 83, No. 4, April 1995, pp. 595-606.								
QG	RR	Nose et al., Two Schemes to Reduce Interconnect Delay in Bi-directional and Uni-directional Buses, 2001 Symposium on VLSI Circuits, Digest of Technical Papers, pp. 193-194.								
QG	SR	PCB Design Guidelines for Reduced EMI, Texas Instruments, SZZA009, November 1999, pp. i-iv and 1-19.								
QG	TR	Sato et al., A 5-Gbyte/s Data-Transfer Scheme With Bit-to-Bit Skew Control For Synchronous DRAM, IEEE Journal of Solid State Circuits, vol. 34, No. 5, May 1999, pp. 653-660.								

Examiner

Date Considered: 5/18/05

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

Notice of References Cited

Application/Control No.

10/022,856

Applicant(s)/Patent Under
Reexamination
PECTEAU ET AL.

Examiner

Qutub Ghulamali

Art Unit

2637

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,611,538	08-2003	Malerevich et al.	370/503
	B	US-6,128,337	10-2000	Schipper et al.	375/229
	C	US-2005/0069041	03-2005	Lincoln, Daniel J.	375/257
	D	US-6,128,347	10-2000	Nomura, Masahiro	375/257
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mikazuki, T.; Matsui, N.; "Statistical design techniques for high-speed circuit boards with correlated structure distributions", IEEE, Transaction on components, Packaging and Manufacturing technology, Vol. 14, Issue 3, Sept. 1991, pages 512-517
	V	Mizuno et al, "Clock Distribution Networks with On-Chip Transmission Lines", IEEE International Conference, June 5-7, 2000, pages 3-5
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

EXHIBIT E



Pillsbury
Winthrop
Shaw
Pittman LLP

1600 TYSONS BOULEVARD
MCLEAN, VA 22102

August 29, 2005

James A. Ballanger
703.905.2112

james.ballanger@pillsburylaw.com

Mr. C. Marc Benoit
Hyperchip, Inc.
1800 Rene-Levesque blvd., W.
Montreal, Quebec H3H 2H2
CANADA

Re: Hyperchip, Inc. - 38700/290547

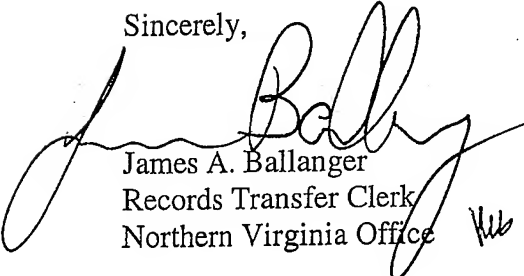
Dear Mr. Benoit:

Enclosed herewith you will find correspondence from the United States Department of Commerce, Patent and Trademark Office and/or the client.

Date	Application No.
08/25/05	10/022,856

You are advised that Pillsbury Winthrop Shaw Pittman, LLP assumes no further responsibility for these files including, but not limited to, future prosecution and/or annuities. **Please file the appropriate documents to ensure that future correspondence is sent directly to you.**

Sincerely,



James A. Ballanger
Records Transfer Clerk
Northern Virginia Office

Enclosures

EXHIBIT F



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,856	12/20/2001	Karl Fecteau	P. 290547	1436
909	7590	01/18/2006	EXAMINER	
PILLSBURY WINTHROP SHAW PITTMAN, LLP			GHULAMALI, QUTBUDDIN	
P.O. BOX 10500			ART UNIT	
MCLEAN, VA 22102			PAPER NUMBER	
			2637	

DATE MAILED: 01/18/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

RECEIVED
PILLSBURY WINTHROP SHAW PITTMAN

JAN 23 2006

CL _____ MT# _____
ATTASj _____
D.C.F. _____
DNT SY(T) _____ (2) _____

Notice of Abandonment

Application No.

10/022,856

Examiner

GHULAMALI

Applicant(s)

Fecteau

Art Unit

2637

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

This application is abandoned in view of:

1. ☐ Applicant's failure to timely file a proper reply to the Office letter mailed on _____.
 - (a) ☐ A reply was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply (including a total extension of time of _____ month(s)) which expired on _____.
 - (b) ☐ A proposed reply was received on _____, but it does not constitute a proper reply under 37 CFR 1.113 (a) to the final rejection.
(A proper reply under 37 CFR 1.113 to a final rejection consists only of: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114).
 - (c) ☐ A reply was received on _____ but it does not constitute a proper reply, or a bona fide attempt at a proper reply, to the non-final rejection. See 37 CFR 1.85(a) and 1.111. (See explanation in box 7 below).
 - (d) ☐ No reply has been received.
2. ☒ Applicant's failure to timely pay the required issue fee and publication fee, if applicable, within the statutory period of three months from the mailing date of the Notice of Allowance (PTOL-85).
 - (a) ☐ The issue fee and publication fee, if applicable, was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the statutory period for payment of the issue fee (and publication fee) set in the Notice of Allowance (PTOL-85).
 - (b) ☐ The submitted fee of \$_____ is insufficient. A balance of \$_____ is due.
The issue fee required by 37 CFR 1.18 is \$_____. The publication fee, if required by 37 CFR 1.18(d), is \$_____.
 - (c) ☒ The issue fee and publication fee, if applicable, has not been received.
3. ☐ Applicant's failure to timely file corrected drawings as required by, and within the three-month period set in, the Notice of Allowability (PTO-37).
 - (a) ☐ Proposed corrected drawings were received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply.
 - (b) ☐ No corrected drawings have been received.
4. ☐ The letter of express abandonment which is signed by the attorney or agent of record, the assignee of the entire interest, or all of the applicants.
5. ☐ The letter of express abandonment which is signed by an attorney or agent (acting in a representative capacity under 37 CFR 1.34(a)) upon the filing of a continuing application.
6. ☐ The decision by the Board of Patent Appeals and Interference rendered on _____ and because the period for seeking court review of the decision has expired and there are no allowed claims.
7. ☐ The reason(s) below:

slk

Petitions to revive under 37 CFR 1.137(a) or (b), or requests to withdraw the holding of abandonment under 37 CFR 1.181, should be promptly filed to minimize any negative effects on patent term.

Attachment to Notice of Abandonment

For questions concerning the notice contact

Office of Patent Publication

Image Assistance Center: 888-786-0101.

Information is also available on the USPTO Internet web site:

<http://www.uspto.gov/web/patents/pubs/abandonnotice.html>

Respond to the Notice of Abandonment by one of the following:

1. Petition To Withdraw Holding of Abandonment (See MPEP 711.03(c) I and 37 CFR § 1.181) No fee required

Where an applicant contends that the application is not in fact abandoned (e.g., a reply was in fact filed), a petition under 37 CFR § 1.181(a) requesting withdrawal of the holding of abandonment is the appropriate course of action. Any petition under 37 CFR § 1.181 to withdraw the holding of abandonment not filed within 2 months of the mail date of a Notice of Abandonment may be dismissed as untimely under 37 CFR § 1.181(f). In order for a petition to be granted, the evidence must be sufficient according to 37 CFR § 1.8(b) Certificate of Mailing, 37 CFR § 1.10 "Express Mail" mailing, or MPEP 503 Postcard Receipt as Prima Facie Evidence. The petition should be addressed as follows:

By mail: Mail Stop: Issue Fee, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450

By facsimile: 703-872-9306

2. Petition To Withdraw Holding Of Abandonment Based On Failure To Receive Office Action (MPEP 711.03(c) II and 37 CFR § 1.181). No fee required

Where an applicant contends that the original Notice of Allowance and Fee(s) Due was never received, if adequately supported, the Office may grant the petition and remail the Office action. The showing required establishing non-receipt of an Office communication must include a statement from the practitioner stating that the Office communication was not received and attesting to the fact that a search of the file jacket and docket records indicates that the Office communication was not received. A copy of the docket record where the nonreceived Office would have been entered had it been received and docketed must be attached to and referenced in practitioner's statement.

Petition should be addressed to the Technology Center handling the application as follows:

By mail: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450

By facsimile: 703-872-9306

3. Petition To Revive An Abandoned Application (See MPEP 711.03(c) III)

Where there is no dispute as to whether an application is abandoned (e.g., the applicant's contentions merely involve the cause of abandonment) a petition under 37 CFR § 1.137 (a) or (b) (accompanied by the appropriate petition fee) is necessary to revive the abandoned application. The text of these rules is available on the USPTO Internet Web site. Forms for these petitions, "Petition For Revival Of An Application For Patent Abandoned Unavoidably Under 37 CFR § 1.137(a)," PTO/SB/61, and "Petition For Revival Of An Application For Patent Abandoned Unintentionally Under 37 CFR 1.137(b)," PTO/SB/64, are available in the forms section of the USPTO website: <http://www.uspto.gov>.

Petitions under 37 CFR § 1.137 should be addressed to the Office of Petitions as follows:

By mail: Mail Stop Petition, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450

By facsimile: 703-872-9306

Note: Abandonment takes place by operation of law for failure to reply to an Office action or timely pay the issue fee, not by operation of the mailing of a Notice of Abandonment

EXHIBIT G



Pillsbury
Winthrop
Shaw
Pittman^{LLP}

1650 Tysons Boulevard
McLean, VA 22102-4859

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Fax 703.770.7901
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January 26, 2006

Mr. C. Marc Benoit
Hyperchip, Inc.
1800 Rene-Levesque blvd., W.
Montreal, Quebec H3H 2H2
CANADA

Re: Correspondence relating to Hyperchip, Inc:38700/290547.

Dear Mr. Benoit :

Enclosed are documents relating to the above client, transferred to your firm.

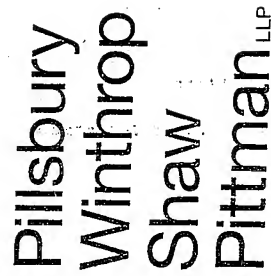
Please ensure the sender of these materials is promptly notified of the transfer to ensure future documents relating to the above matter are sent directly to you. Pillsbury Winthrop Shaw Pittman LLP assumes no further responsibility for the above matter.

Sincerely,

K. Edward Fisher
Facilities and Records Manager

Enclosure

EXHIBIT H

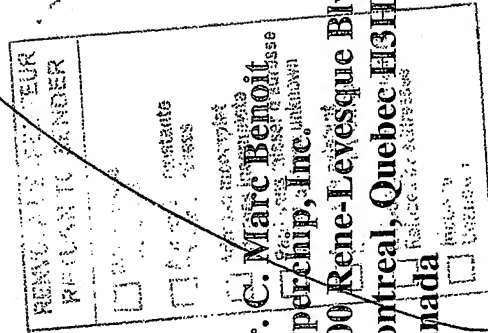


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FIRST CLASS MAIL

[illegible]

WILLIAM